

REMARKS/ARGUMENTS

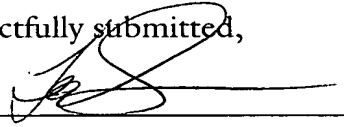
Claims 1-21 are pending in the application. The specification has been amended to correct clerical errors introduced in the preparation of the application. No new matter is added.

Attached hereto is a marked-up version of the changes made to the specification and claims by the current amendment. The attached page is captioned "Version with markings to show changes made."

In view of the above, each of the presently pending claims in this application is believed to be in immediate condition for allowance. Accordingly, the Examiner is respectfully requested to pass this application to issue.

Dated: April 24, 2003

Respectfully submitted,

By 

Thomas J. D'Amico

Registration No.: 28,371

DICKSTEIN SHAPIRO MORIN &
OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

Attorneys for Applicant

Version With Markings to Show Changes Made

Please rewrite the paragraph beginning on page 6, lines 5-11 as follows:

After signal sampling is completed, the voltage on VDD line N-1 drops to 0 at 224 during the reset time for photodiode [225]223 for line N-1. This means that the floating diffusion for that photodiode [225]223 will be charged to the reset level when the output column is grounded and the surface potential under the source follower gate is minimum. This may increase the cell capacitance.